

Optical Clock Distribution To VLSI Chips

By Joseph W. Goodman

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Optical Clock Distribution To Silicon Chips. This paper includes the presentation of an optical clock distribution
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Our experience with optical clock distribution to date has been nology an optical chip-to-chip interconnect requires
Joseph W. Goodman,

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Abstract Investigations of the use of optical for distributing the clock to a CMOS chip are reported. Two chips were designed, the first incorporating a set of

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and no surplusage, incorporating all needed functions, and eliminating a requirement for any external chips. This is demanding of time and resources,

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Timing Uncertainty For Receivers In Optical Clock Distribution For Joseph W. Goodman. to reduce signal skew for clock distribution for silicon VLSI chips.

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